

And the mile than the third than the third that the

The state of the s

FIG. 6 **FROM DECODE** 46W 34 WRITE POINTER/LOGIC 32 44 45 47 **ENTRYO** ENTRY1 ENTRY2 ENTRY3 **ENTRY4 IDLE** SYM TO DEST READ POINTER/LOGIC 46R READ/WRITE LOGIC <u>48</u> 62 -56 54 <u>50</u>0 **EDGE SYNC** DET IN **CLR** -RV0 **CLR RDWO** WRW0 S **EDGE SYNC** WV0 **→** R DET IN IN **CLR CLR** 60 58-52 501 WRW1 RCV/TX RV1 VALID LOGIC W1 -RDW1 502 WRW2 RCV/TX RV2 VALID LOGIC WV2 -RDW2 503 WRW3 -RV3 RCV/TX VALID LOGIC WV3 -RDW3 504 WRW4 RCV/TX RV4 VALID LOGIC WV4 · RDW4 DIVIDED RECOVERED RINGCLK/2 **CLOCK DOMAIN** DOMAIN

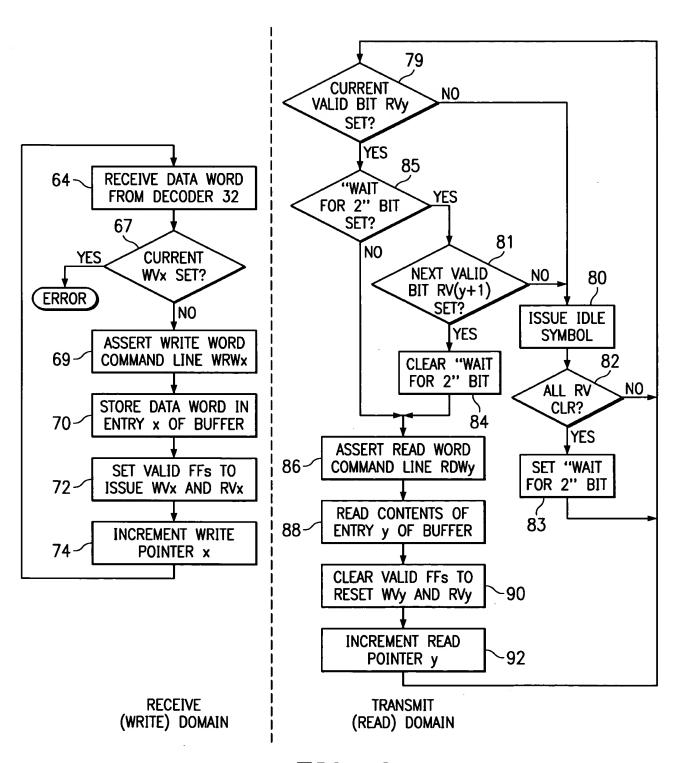


FIG. 8